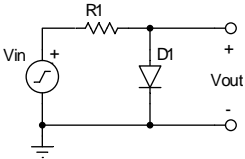
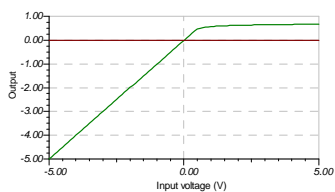
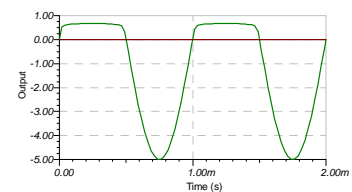
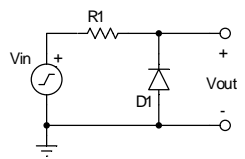
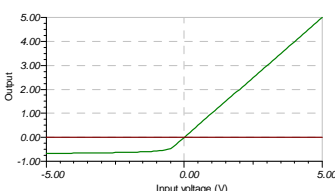
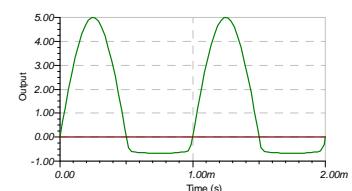
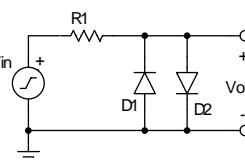
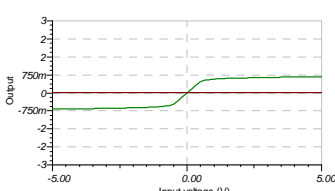
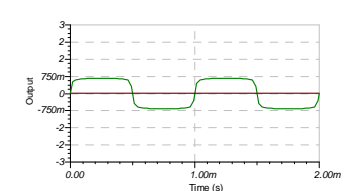
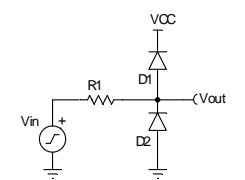
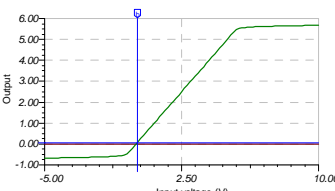
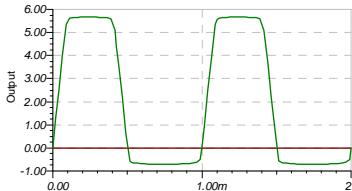
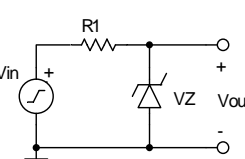
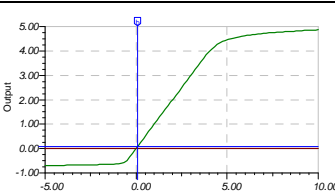
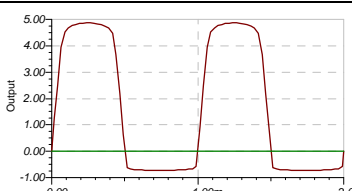
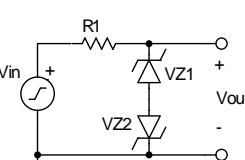
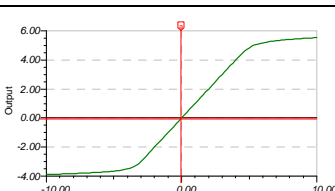
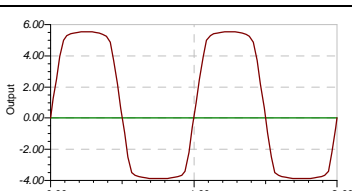
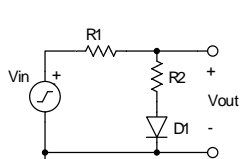
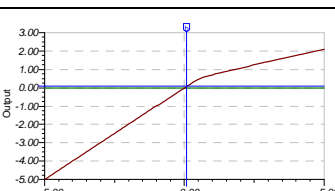
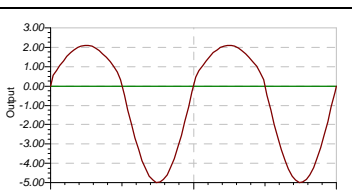
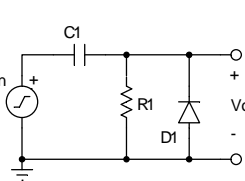
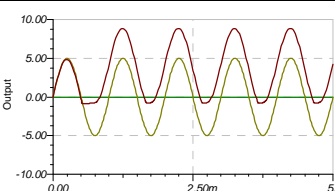
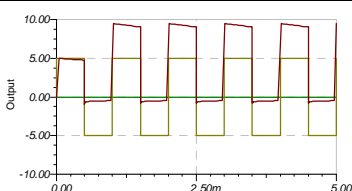


Circuit	Voltage Transfer Characteristic	Sinusoidal Test Signal Output	Description
			<p>Positive Limiter/Clipper:</p> <p>Clips the positive half of a voltage waveform that is greater than 0.7V.</p>
			<p>Negative Limiter/Clipper:</p> <p>Clips the negative half of a waveform that is less than -0.7V.</p>
			<p>Double Limiter/Clipper:</p> <p>Clips both halves of a voltage waveform exceeding +/- 0.7V.</p>
			<p>Electro-Static Discharge (ESD) protection circuit:</p> <p>Prevents the signal from exceeding the power supply range by more than 0.7V.</p> <p>$-0.7 < V_{out} < (V_{CC} + 0.7)$</p>
			<p>Zener Limiter / Clipper</p> <p>$-0.7 < V_{out} < V_z$</p>
			<p>Dual Zener Limiter / Clipper</p> <p>$-(V_{Z2} + 0.7) < V_{out} < (V_{Z1} + 0.7)$</p>
			<p>Soft Limiter:</p> <p>Attenuates the portion of the signal exceeding the voltage limit by a factor determined by the voltage divider.</p>
			<p>DC Restore / Voltage Clamp</p> <p>Shifts signal up by $(V_{pk} - 0.7V)$. Used in clock recovery circuits.</p>