IMPEDANCE AT A NODE (GENERAL) (USE FOR RIM, ROUT OF AMPLIFIER CIRCUITS)

PROCEDURE

1 SUPPRESS (SET = 0) ALL INDEPENDENT SOURCES

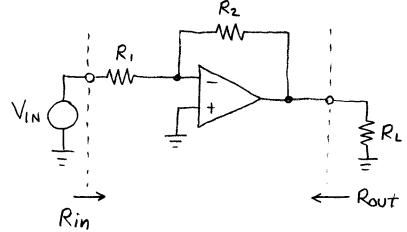
V SOURCE - SHORT NEED TO KEEP

I SOURCE > OPEN DEPENDENT SOURCES

② APPLY TEST SOURCE Vx) OR, IF EASIER, APPLY ix AND CALCULATE Vx

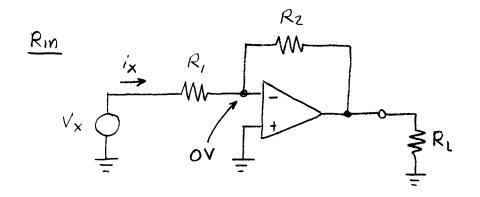
4 IMPEDANCE AT NODE $\Rightarrow Z = \frac{V_X}{i_Y}$

EXAMPLE INVERTING OP-AMP CIRCUIT Rin, Rout



WHAT Rin DOES VIN SEE?

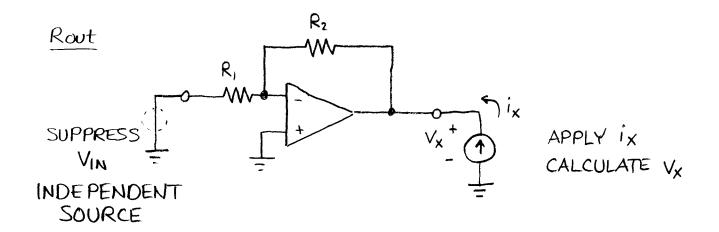
WHAT ROUT IS DRIVING LOAD R. ?



VIRTUAL GROUND: V = O (ASSUME IDEAL OP-AMP)

OHM'S LAW FOR R1:
$$i_X = \frac{V_X - O}{R_1} = \frac{V_X}{R_1}$$

SOLVE FOR
$$\frac{Vx}{ix} = R_1 \Rightarrow \boxed{R_{in} = R_1}$$



INVERTING AMPLIFIER WITH ZERO INPUT:

$$\frac{\sqrt{x}}{ix} = 0 \Rightarrow \boxed{\text{Rout} = 0}$$