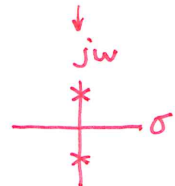


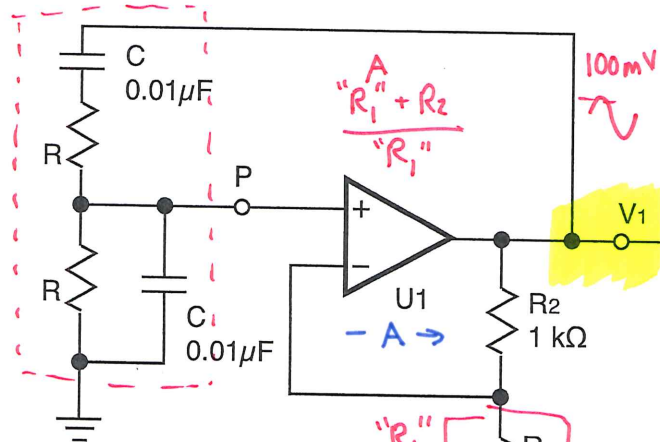
STABLE AMPLITUDE \sim



$V_i \rightarrow V_{out} \phi$ CHANGES
AGC LOOP UNCHANGED

V_R TOO SMALL: $AV_G I_S < I_{SMA} \Rightarrow V_G \uparrow$

B NETWORK



100mV

x50

5V

VOUT

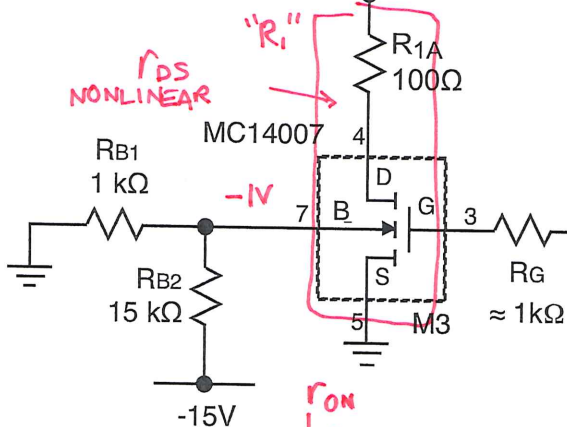
AVG OF i_S OVER 1 CYCLE

$I_E = i_S - I_{SMA}$

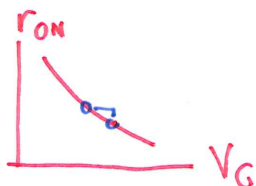
KCL DOES SUBTRACTION

$$V_C = \frac{1}{C_1} \int I_E dt$$

DRIVES AVERAGE OF $I_E \Rightarrow 0$



CONTROLS "A" GAIN



MOSFET: $V_G \uparrow \Rightarrow r_{ON} \downarrow \Rightarrow A \uparrow$: MOVES POLES TOWARD RHP \sim

Figure 6.6